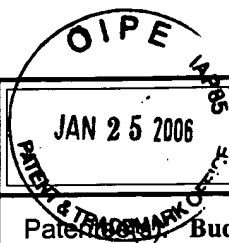


10/721966 C9C



**TRANSMITTAL LETTER**  
**(General - Patent Issued)**

Docket No.  
**END920030108US1**

Patent No. **Budell et al.**

U.S. Patent No.

**6,978,214 B2**

Issue Date

**December 20, 2005**

Title: **VALIDATION OF ELECTRICAL PERFORMANCE OF AN ELECTRONIC PACKAGE PRIOR TO FABRICATION**

COMMISSIONER FOR PATENTS:

**Certificate**

**JAN 27 2006**

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**Jack P. Friedman, Ph.D.**  
**Reg. No. 44,688**  
**Schmeiser, Olsen & Watts**  
**3 Lear Jet Lane, Suite 201**  
**Latham, NY 12110**  
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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,978,214 B2

DATED : December 20, 2005

INVENTOR(S) : Budell et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14

Line 49, delete "Delancy" and insert --Delaney--

Column 15

Line 37, delete " $N \geq 1$ " and insert -- $N \geq$ --

Line 55, delete "step c" and insert --step e--

Line 56, delete "incrementing J" and insert --incrementing I--

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Reg. No. 44,688  
Schmeiser, Olsen & Watts  
3 Lear Jet Lane, Suite 201  
Latham, NY 12110

PATENT NO. 6,978,214 B2

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PATENT NO. : 6,978,214 B2

DATED : December 20, 2005

INVENTOR(S) : Budell et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please delete, "Drawing Sheets 1 ,2 ,3 ,4, and 9" because of the reprint quality.

Please insert --Replacement Drawing Sheets 1, 2 ,3 ,4, and 9--. They are attached to this page.

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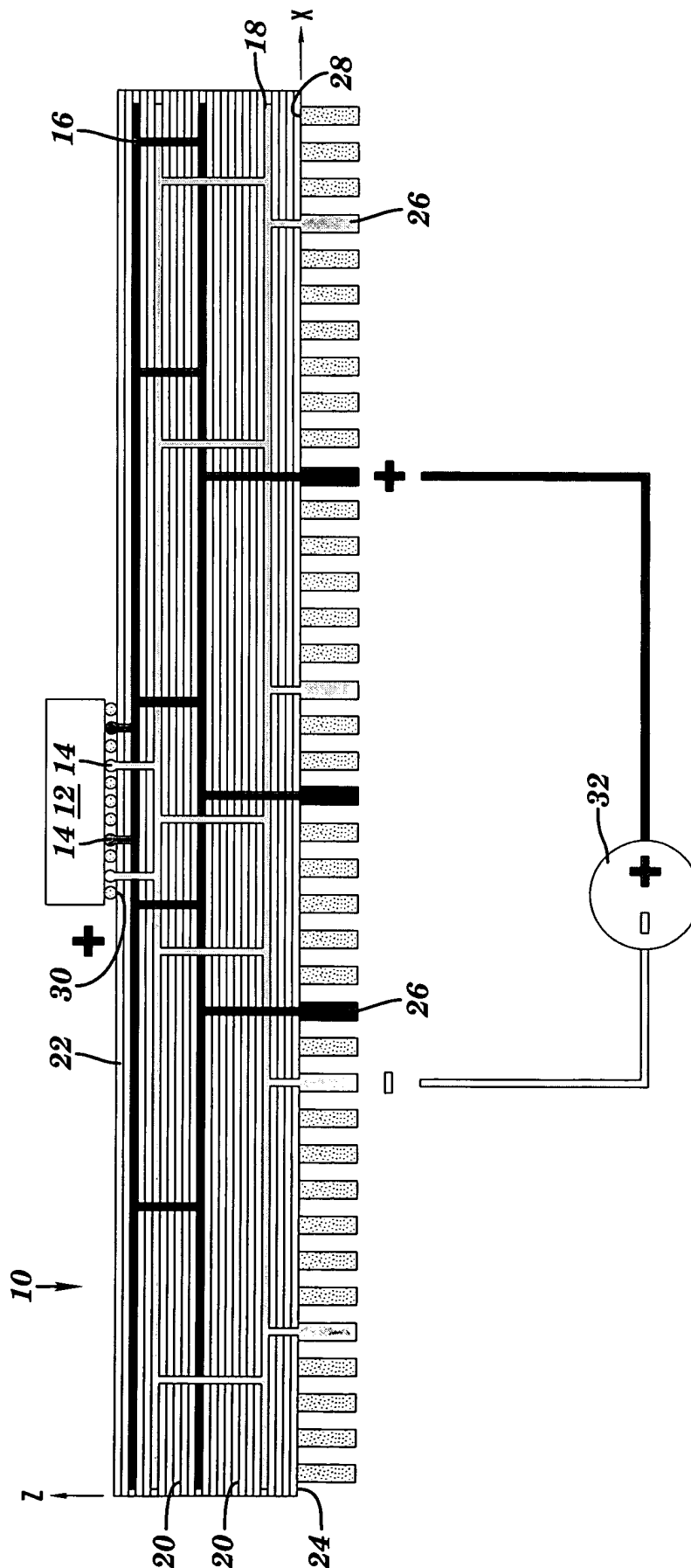
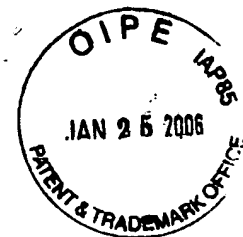
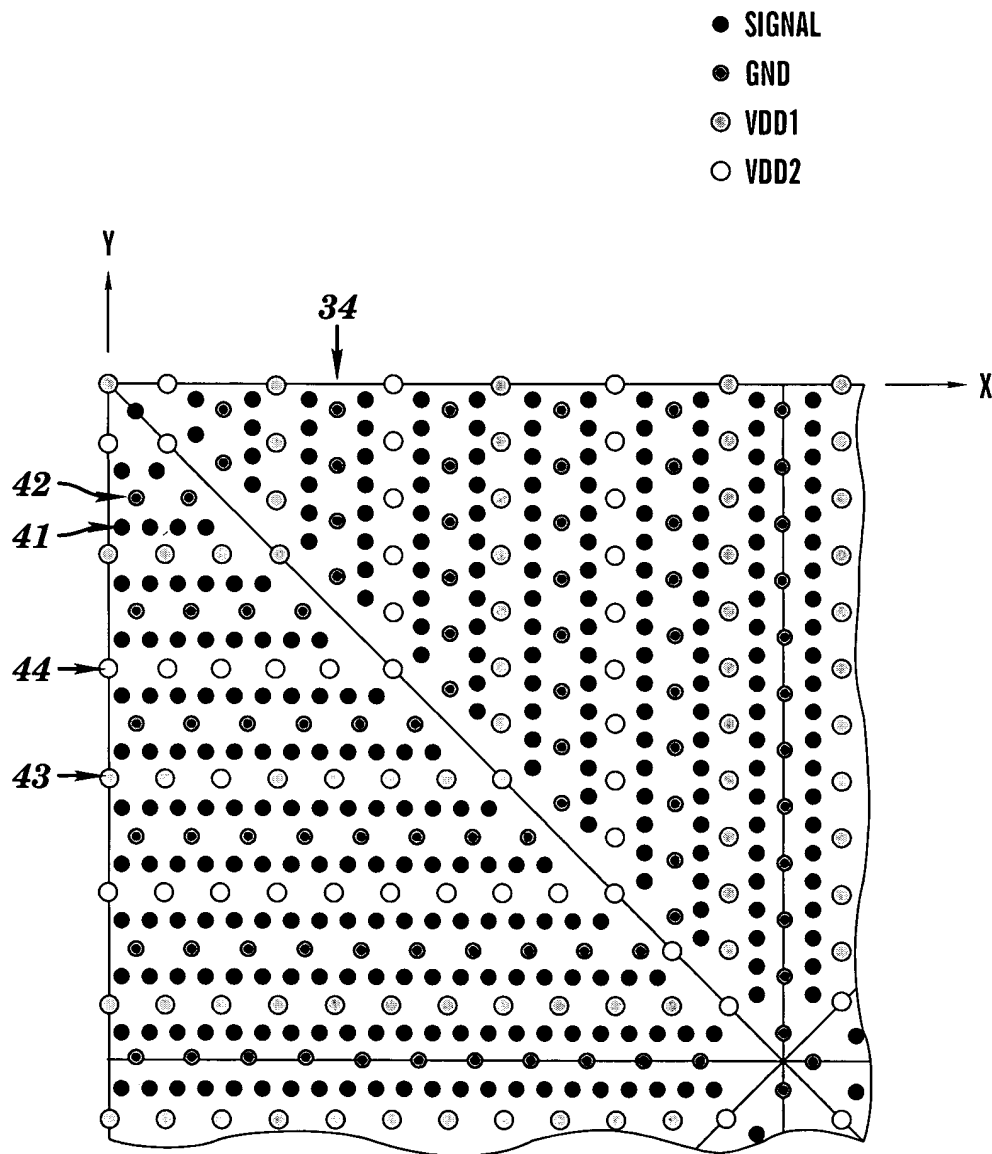
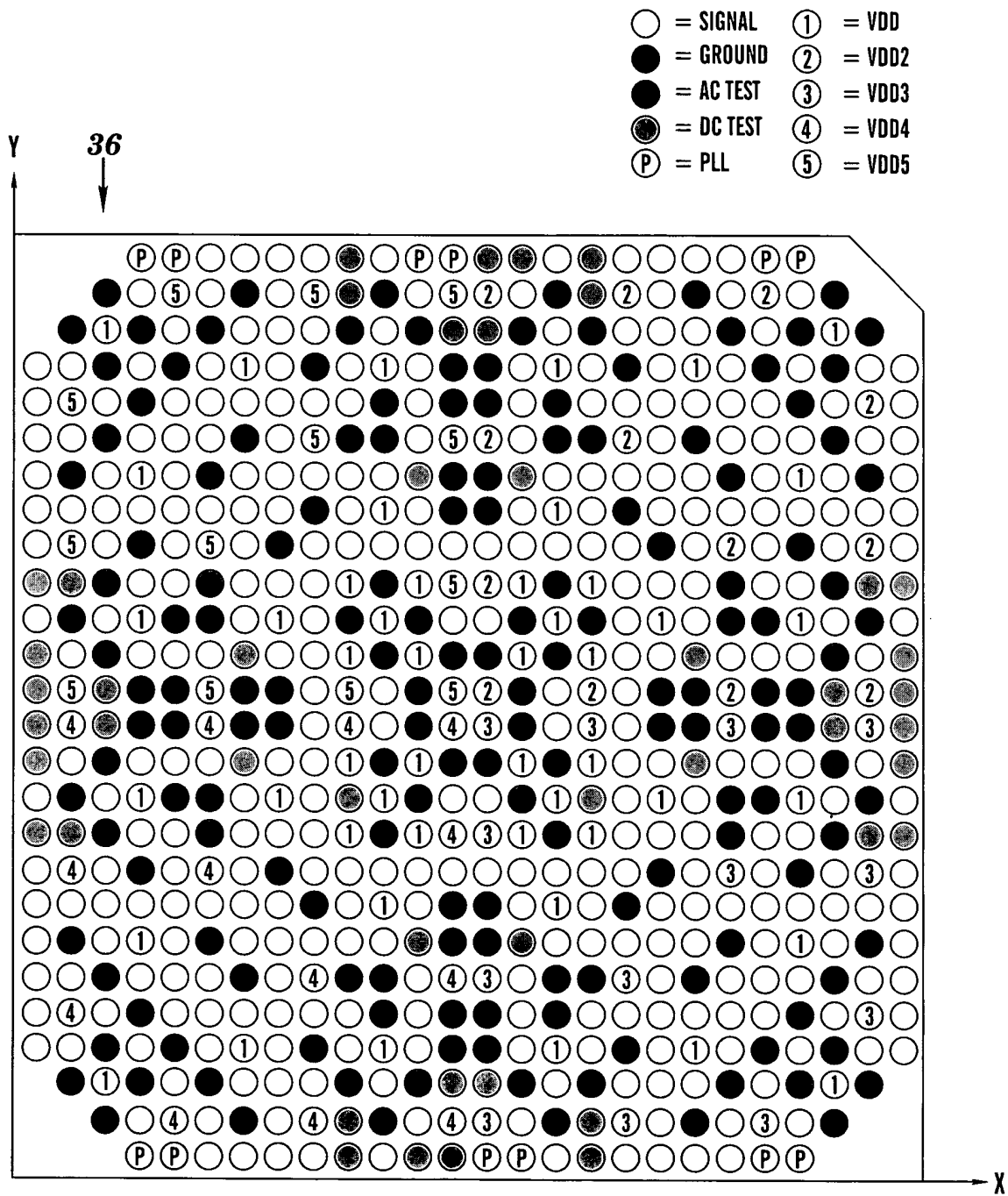


FIG. 1

**FIG. 2**

**FIG. 3**

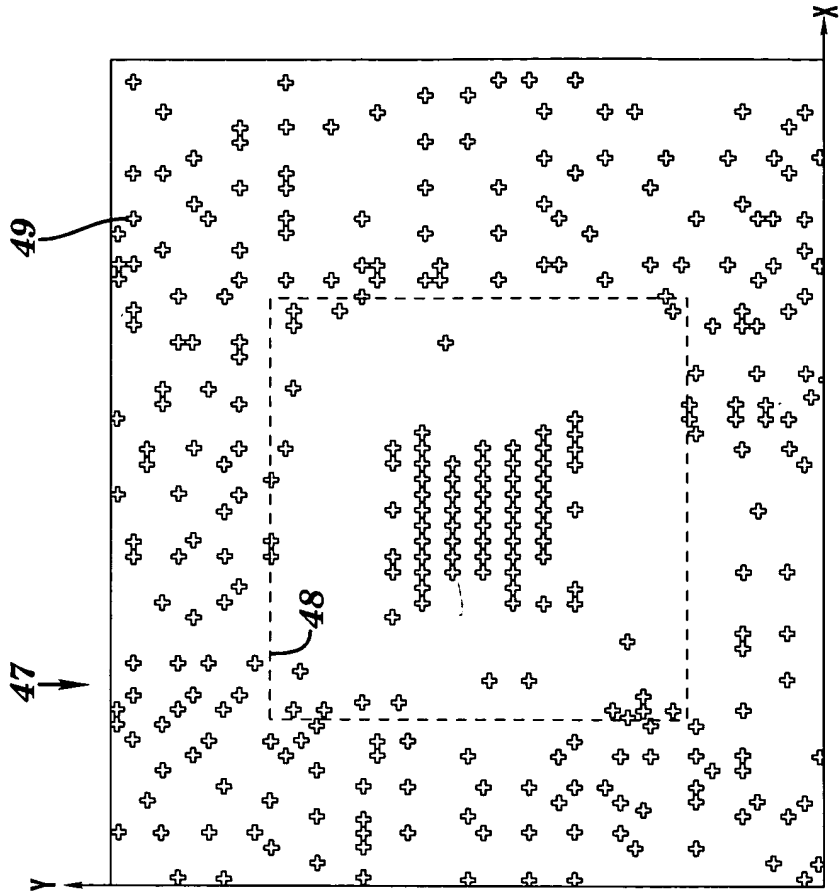


FIG. 4A

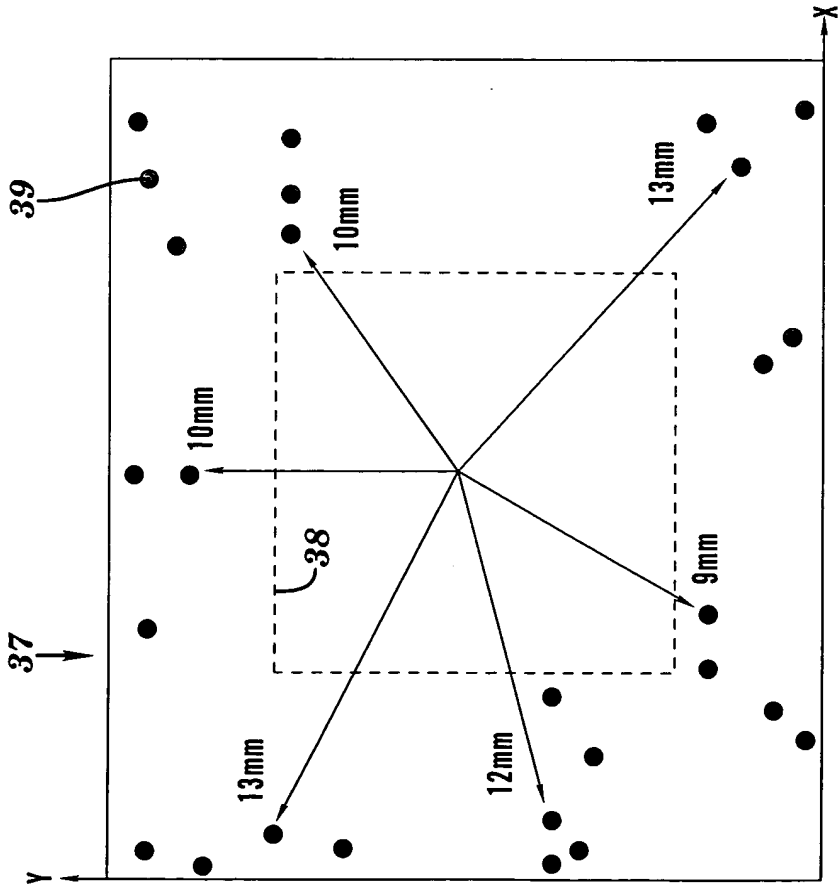


FIG. 4B

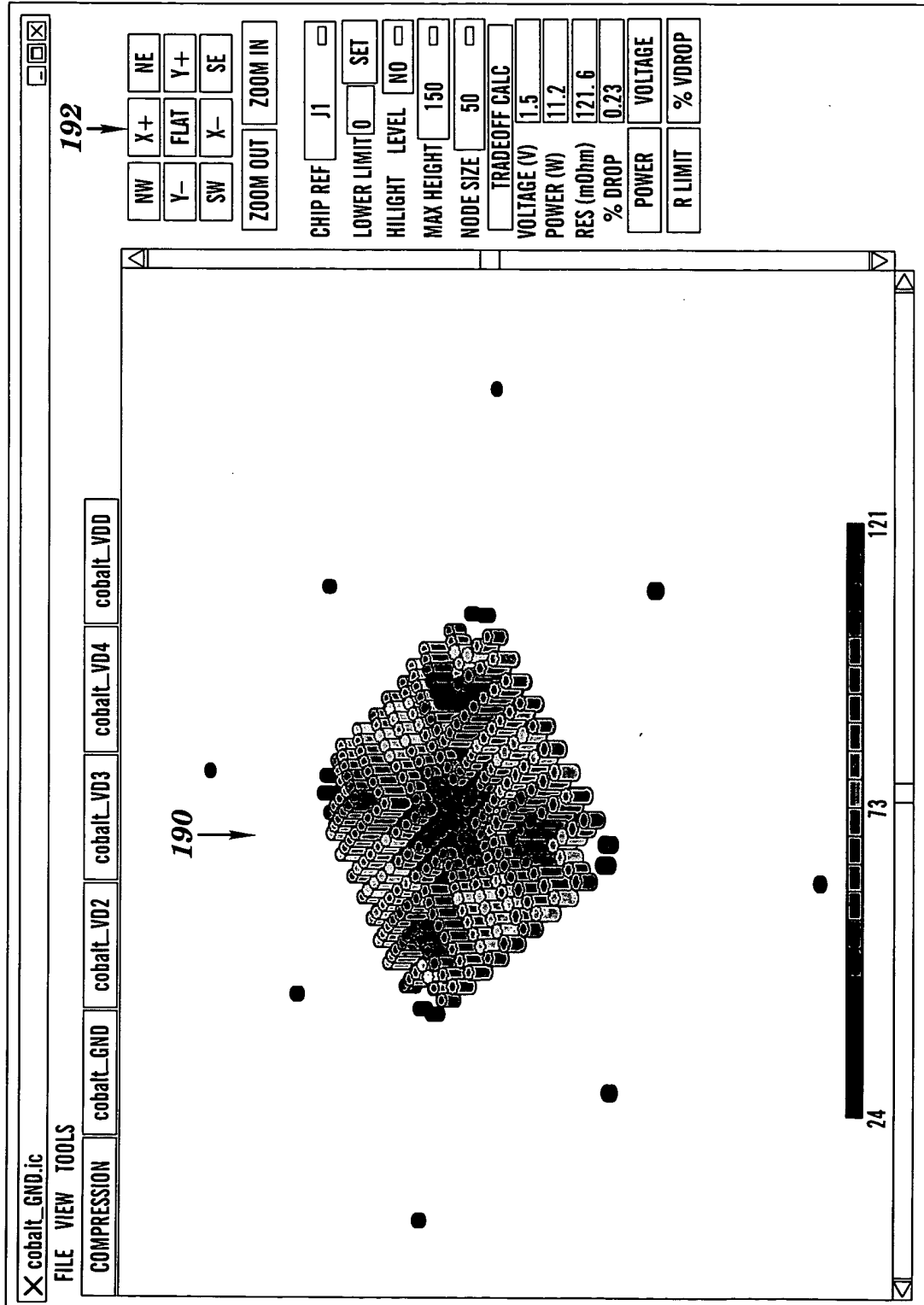


FIG. 10